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09/593,406	06/14/2000	Dirk Rosenau	P00,1187 8262	
7590 04/06/2004			EXAMINER	
SCHIFF, HARDIN & WAITE			ABDI, KAMBIZ	
Patent Department 71st Floor Sears Tower			ART UNIT	PAPER NUMBER
233 South Wacker Drive Chicago, IL 60606			3621	
			DATE MAILED: 04/06/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

	L Application No.	Annilanda)				
	Application No.	Applicant(s)				
	09/593,406	ROSENAU ET AL.				
Office Action Summary	Examiner	Art Unit				
	Kambiz Abdi	3621	M4)			
The MAILING DATE of this communication appeariod for Reply	pears on the cover sheet with the o	orrespondence ac	ddress			
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a rep If NO period for reply is specified above, the maximum statutory period Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailin earned patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a reply be tirnly within the statutory minimum of thirty (30) day will apply and will expire SIX (6) MONTHS from e, cause the application to become ABANDONE	mely filed ys will be considered time the mailing date of this of (35 U.S.C. § 133).				
Status						
1)⊠ Responsive to communication(s) filed on <u>01 F</u>	February 2004.					
2a) This action is FINAL . 2b) ∑ This	s action is non-final.					
3) Since this application is in condition for allowa	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice under	Ex parte Quayle, 1935 C.D. 11, 4	53 O.G. 213.				
Disposition of Claims						
4) ☐ Claim(s) 1-14 is/are pending in the application 4a) Of the above claim(s) is/are withdra 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 1-14 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or Application Papers 9) ☐ The specification is objected to by the Examin 10) ☐ The drawing(s) filed on is/are: a) ☐ accompany and applicant may not request that any objection to the Replacement drawing sheet(s) including the correction.	ewn from consideration. or election requirement. er. cepted or b) objected to by the edrawing(s) be held in abeyance. Se	ee 37 CFR 1.85(a).	CFR 1.121(d).			
11) The oath or declaration is objected to by the E						
Priority under 35 U.S.C. § 119						
 12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority documents. 2. Certified copies of the priority documents. 3. Copies of the certified copies of the priority documents. * See the attached detailed Office action for a list. 	nts have been received. Its have been received in Applicatority documents have been received in Rule 17.2(a)).	tion No ved in this Nationa	ıl Stage			
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08 Paper No(s)/Mail Date	4) Interview Summar Paper No(s)/Mail [6] 5) Notice of Informal 6) Other:	Date	ΓΟ-152)			

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DETAILED ACTION

 The prior office actions mailed on earlier dates are incorporated herein by reference. In particular, the observations with respect to claim language, and response to previously presented claims and arguments.

Specification has been amended based on the informalities pointed out by the examiner.

Claims 1 and 9 have been amended.

No new claims have been added.

Claims 1-14 are pending.

Response to Arguments

2. Applicant's arguments with respect to independent claims 1 and 9 have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. Claims 1, 2, 6, 9, 10, 12, 13, and 14 are rejected under 35 U.S.C. 103(a) as being unpatentable over U.S. Patent No. 4,858,138 to Paul C. Talmagdge in view of U.S. Patent No. 4,649,266 to Alton B. Eckert.

As for claim 1 and 9, while Talmadge discloses; a franking system and method comprising;

a security module containing a first program memory in which a first program is stored and a security module data processing unit connected to said first program memory and being programmed by

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said first program to calculate a multi-byte security code from existing system data and to be able to receive new system data to modify said existing system data (See Talmadge figures 1-6 and 11 and associated text, col. 2, ln. 62-68, col. 3, ln. 1-32, col. 5, ln. 5-14, col. 8, ln. 21-47, and col. 18, ln. 1-55);

said security module data processing unit being further programmed by said first program to, immediately upon receipt of said new system data, validate said new system data and determine whether said new system data are required for said security code and, if so, to immediately begin recalculating said security code in a first routine and, in a second routine, to finish recalculating said security code for at least one security imprint, thereby producing a recalculated security code, and to initiate an accounting operation for said monetary value and to communicate the recalculated security code to said separate data processing unit (See Talmadge figures 1-6 and 11 and associated text, col. 2, ln. 62-68, col. 3, ln. 1-32, col. 5, ln. 5-14, col. 8, ln. 21-47, and col. 18, ln. 1-55);

said separate data processing unit, upon receiving said recalculated security code, compiling said print image containing said recalculated security code as a security imprint and embodying said monetary value (See Talmadge figures 1-6 and 11 and associated text, col. 2, ln. 62-68, col. 3, ln. 1-32, col. 5, ln. 5-14, col. 8, ln. 21-47, and col. 18, ln. 1-55).

Talmadge clearly teaches the step of a separate data processing unit disposed externally of said security module (The vault 52), said separate data processing unit being programmed to edit print data to compile a print image that contains said security code as a security imprint (See Talmadge figures 1-6 and 11 and associated text, col. 2, ln. 62-68, col. 3, ln. 1-32, col. 5, ln. 5-14, col. 8, ln. 21-47, and col. 18, ln. 1-55).

What is not clear in Talmadge reference is that in step 43 of figure 4 how the "validation number" is generated. Eckert clearly teaches the steps involved in the generation of the security code that is passed on to the secondary processor to generate the postage value imprint (See Eckert figures 1,2, and 3, and col. 2, In. 1-68). Examiner takes Official Notice that using "multi-byte security code" is old and well established in the postage metering systems for further security.

Therefore, it would have been obvious to one having ordinary skill in the art at the time the current invention was made to combine the above teachings to achieve a better authorization procedure

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and more reliable validation system for the postal franking systems which would take less time to process by division of task between the two processors, which this division of task is a well known practice in calculation intensive algorithm that need fast results.

As for claim 2, Talmadge and Eckert disclose, all the limitations of claim 1, further;

What Talmadgel does not explicitly teach is that a security code is a data authorization code and wherein said security module data processing unit contains an internal non-volatile memory in which at least one key for calculating said data authorization code is protectively stored against access and wherein said security module contains a further security module data processing unit for performing said accounting. However, both Talmadge and Eckert clearly teaches the method of creating a security code to be imprinted on the mail item and keeping an accounting of the available fund to the franking system by means of a data processing module (See Talmadge figures 1-6 and 11 and associated text, col. 2, ln. 62-68, col. 3, ln. 1-32, col. 5, ln. 5-14, col. 8, ln. 21-47, and col. 18, ln. 1-55, and Eckert figures 1,2, and 3, and col. 2, ln. 1-68). Therefore, it would have been obvious to one having ordinary skill in the art at the time the current invention was made to combine the above teachings to achieve a more reliable and efficient processing system and method for tracking and securing the postal franking systems.

As for claim 10, Talmadge and Eckert disclose, all the limitations of claim 9, further;

Talmadge, disclose the claimed invention, as discussed above, except Talmadge is not specific on the step of, calculating a data authorization code in said security module data processing unit as said security code dependent on said ascending register value and additional data in said new system data and generating said security imprint at a time following an end of entry of said new system data and before conducting an accounting for said monetary value. Examiner submits that It would have been an obvious matter of design choice to modify the teachings of Talmadge, to provide the step of, using an ascending register in order to calculate an authorization code, since applicant has not disclosed that using ascending register solves any stated problem in a new or unexpected way or is for any particular purpose which is unobvious to one of ordinary skill and it appears that the claimed

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feature does not distinguish the invention over similar features in the prior art since, the teachings of Talmadge will perform the invention as claimed by the applicant with any method, means, or product to use the ascending register as a variable for generating security code.

As for claim 12, Talmadge and Eckert disclose, all the limitations of claim 9, further;

Talmadge teaches how and authorization code can be calculated and there is certain precalculations that do take place in their system. But, Talmadge does not explicitly disclose that said
security code is a data authorization code and wherein said security module data processing unit
calculates said data authorization code dependent on a machine identifier, said monetary value and
a current date, and wherein at least said machine identifier is included in a pre-calculation of n bytes
of said data authorization code. However, Eckert is clearly discloses the steps and process to obtain
a authorization code base on the elements that have been mention above and certain precalculations that has been mentioned (See Eckert figures 1,2, and 3). Therefore, it would have been
obvious to one having ordinary skill in the art at the time the current invention was made to combine the
above teachings to achieve further strength in the cryptography of the validation code to be printed by the
postal franking systems in addition to speed up overall processing of information such as calculation of an
authorization code to be printed by a franking system. Preprocessing of certain calculations for speeding
up further processing in a system is well known in the art.

As for claim 13, Talmadge and Eckert disclose, all the limitations of claim 9, further;

Talmadge teaches how and authorization code can be calculated and there are certain precalculations that do take place in the system. But, what Talmadge is not explicit on that said security code
is a data authorization code and wherein said security module data processing unit calculates said data
authorization code dependent on a machine identifier, said monetary value and a current date, and
wherein at least said machine identifier and said date is included in a pre-calculation of n bytes of said
data authorization code. However, Eckert clearly teaches a security module capable of creating an
authorization code for postal franking that does pre-calculations of data before the next variable has

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arrived (See Eckert figures 1,2, and 3, and col. 2, ln. 1-68). Therefore, it would have been obvious to one having ordinary skill in the art at the time the current invention was made to combine the above teachings to achieve further strength in the cryptography of the validation code to be printed by the postal franking systems in addition to speed up overall processing of information such as calculation of an authorization code to be printed by a franking system. Preprocessing of certain calculations for speeding up further processing in a system is well known in the art.

As for claim 14, Talmadge and Eckert disclose, all the limitations of claim 9, further;

Talmadge teaches how and authorization code can be calculated and there are certain precalculations that do take place in their system. But, what Talmadge is not explicit is comprising
successively supplying sets of new system data to said security module data processing unit and after
communicating said security code to said separate data processing unit, in said security module data
processing unit beginning calculation of a next-successive security code for next new system data, at
least dependent on said ascending register value to produce pre-calculated n bytes of said nextsuccessive security code. However, Eckert clearly teaches a security module capable of creating an
authorization code for postal franking that does pre-calculations of data before the next variable has
arrived (See Eckert figures 1,2, and 3, and col. 2, ln. 1-68). Therefore, it would have been obvious to
one having ordinary skill in the art at the time the current invention was made to combine the above
teachings to achieve further strength in the cryptography of the validation code in addition to speed up
overall processing of information, such as calculation of an authorization code to be printed by a franking
system. Preprocessing of certain calculations for speeding up further processing in a system is well
known in the art.

5. Claims 6 is rejected under 35 U.S.C. 103(a) as being unpatentable U.S. Patent No. 4,858,138 to Paul C. Talmagdge in view of U.S. Patent No. 4,649,266 to Alton B. Eckert as applied to claim 1 above and further in view U.S. Patent No. 5,671,146 to Harald Windel.

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As for claim 6, Talmadge and Eckert disclose, all the limitations of claim 3, further;

Talmadge and Eckert disclose the claimed invention, as discussed above, except for the step of, internal non-volatile memory, which is an SRAM of said processor, and wherein said security module further comprises a battery supporting said SRAM, and wherein said SRAM had memory areas for protected storage of at least some data produced by said pre-calculation, and wherein said at least one key for calculating said data authorization code is protectively stored in a memory area of said SRAM.

Computer systems typically use magnetic disk drives for mass storage of data. However, disk drives are disadvantageous in that they are bulky and in their requirement for high precision moving mechanical parts. Consequently they are not rugged and are prone to reliability problems, as well as consuming significant amounts of power. Solid-state memory devices such as DRAM's and SRAM's do not suffer from these disadvantages. However, they are much more expensive, and require constant power to maintain their memory (volatile). Consequently, they are typically used as temporary storage.

Cache memory is generally used to speed up the performance of systems having slower access devices. For example in a computer system, access of data from disk storage is slow and the speed would be greatly improved if the data could be obtained from the much faster RAM. Typically a part of system RAM is used as a cache for temporarily holding the most recently accessed data from disk. The next time the data is needed, it may be obtained from the fast cache instead of the slow disk. The scheme works well in situations where the same data is repeatedly operated on. This is the case in most structures and programs since the computer tends to work within a small area of memory at a time in running a program. Another example of caching is the using of faster SRAM cache to speed up access of data normally stored in cheaper but slower DRAM or any other none volatile memory devices. In addition is has been used in the art for security reasons and tamper proofing remote systems that need to be securely placed and prevented from being accessed by unauthorized persons (See Windel (5,671,146) figures 1b, 1c, and 2A and col. 11, ln. 9-52). Therefore, it would have been obvious to one having ordinary skill in the art at the time the current invention was made to combine the above teachings to achieve a faster, more reliable and more efficient processing system for the postal franking systems to

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access data stored in the memory and at the same time more secure housing for the franking device to be tampered with.

6. Claims 3, 4, 5, and 11 are rejected under 35 U.S.C. 103(a) as being unpatentable U.S. Patent No. 4,858,138 to Paul C. Talmagdge in view of U.S. Patent No. 4,649,266 to Alton B. Eckert as applied to claim 1 above and further in view U.S. Patent No. 4,934,846 to Dennis T. Gilham.

As for claim 3, Talmadge and Eckert disclose, all the limitations of claim 2, further;

Though, Gilham discloses that said security module data processing unit is a processor programmed by said first program to calculate a first eight bytes of said data authorization code in advance in said first routine each day, and wherein said further security module data processing unit is a hardware accounting unit which produces an accounting result as a result of said accounting in said second routine, and wherein said security module further contains a non-volatile memory, accessible by said hardware accounting unit, in which said hardware accounting unit stores said accounting result (See Gilham col. 4, In. 1-34). However, it would have been obvious to one having ordinary skill in the art at the time the current invention was made to combine Talmadge, Eckert and Gilham to expedite and create a more secure and better authorization code to be printed by a franking system.

As for claim 4, Talmadge, Eckert and Gilham disclose, all the limitations of claim 3, further;

Though, Talmadge and Eckert disclose that a processor is programmed by (See Eckert figure 2 and 3 and associated text) said first program to determine an ascending register value, dependent on said monetary value, for at least one mail item, and to finish calculating said data authorization code in said second routine for said at least one mail item using said ascending register value. Therefore, it would have been obvious to one having ordinary skill in the art at the time the current invention was made to modify the teachings of Talmadge, to provide the step of, using an ascending register since applicant has not disclosed that using ascending register. It should be noted as well that applicant has not pointed out or disclosed that using ascending register usage solves any stated problem in a new or unexpected

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way or is for any particular purpose which is unobvious to one of ordinary skill and it appears that the claimed feature does not distinguish the invention over similar features in the prior art since, the teachings of Talmadge and Eckert will perform the invention as claimed by the applicant with any method, means, or product to use the ascending register as a variable for generating security code.

As for claim 5, Talmadge and Eckert disclose, all the limitations of claim 3, further;

Though, Gilham disclose that using a plurality of mail items all having the same monetary value for franking or different authorization code based on plurality of variable numbers such as ascending or descending registers or change in the time or date or the article number. Wherein said processor is programmed by said first program to pre-calculate a next-successive data authorization code for a next mail item after debiting said monetary value for a preceding mail item (See Gilham col. 4, In. 1-34). However, it would have been obvious to one having ordinary skill in the art at the time the current invention was made to combine Talmadge, Eckert and Gilham to speed up overall processing of information such as calculation of an authorization code to be printed by a franking system.

As for claim 11, Talmadge and Eckert disclose, all the limitations of claim 9, further;

Talmadge disclose the claimed invention, as discussed above, except for the step of, using a plurality of mail items all having the same monetary value for franking or different authorization code based on plurality of variable numbers such as ascending or descending registers or change in the time or date or the article number. Wherein said new system data are associated with an inserted mail item and wherein said security code is a data authorization code and wherein said security module data processing unit calculates said data authorization code dependent on said ascending register value and additional data in said new system data at a time from said insertion of said mail item and before conducting an accounting for said monetary value. However, Gilham clearly discloses the steps mentioned above for generating an authorization code for postal franking. (See Gilham col. 4, In. 1-34). Therefore, it would have been obvious to one having ordinary skill in the art at the time the current invention was made to combine the above teachings to achieve a more reliable and efficient processing

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system and method for tracking and securing the postal franking systems. In addition, it would speed up the printing process by pre-processing some calculations based on none variable data needed to calculate the authorization code for printing by the postal franking system.

7. Claims 7 and 8 are rejected under 35 U.S.C. 103(a) as being unpatentable over U.S. Patent No. 4,858,138 to Paul C. Talmagdge in view of U.S. Patent No. 4,649,266 to Alton B. Eckert as applied to claim 6 above and further in view of U.S. Patent No. 6,058,193 to Robert A. Cordery.

As for claim 7, Talmadge and Eckert disclose, all the limitations of claim 6, further;

What Talmadge and Eckert are not specific and explicit about is a processor is programmed by said first program to calculate said data authorization code using a machine identifier and OCR key indicator, a date, said monetary value, and a register value for an ascending register. However, Cordery clearly discloses using OCR recognizable characters and the usefulness of OCR Key indicators (or error correction code) (See Cordery figures 6 and 7 and col. 9, In.43-47) for additional indicium's for generating a validation markings or authorization code. Therefore, it would have been obvious to one having ordinary skill in the art at the time the current invention was made to combine the above teachings to achieve a more sophisticated and secure validation code for the postal franking systems to be printed on postage mail.

As for claim 8, Talmadge and Eckert disclose, all the limitations of claim 2, further;

What Talmadge and Eckert are not specific and explicit about is a processor programmed by said first memory to calculate said data authorization code using an algorithm selected from the group consisting of DES algorithms and triplet DES algorithms. However, Cordery clearly teaches the application of "triple DES" algorithm (See Cordery figure 3, col. 8, ln. 14-20 and ln. 50-65). Therefore, it would have been obvious to one having ordinary skill in the art at the time the current invention was made to combine the above teachings to achieve further strength in the cryptography of the validation code to be printed by the postal franking systems to be printed on postage mail.

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8.

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the previous office action for the convenience of the applicant. Although the specified citations are

representative of the teachings in the art and are applied to the specific limitations within the individual

Examiner has pointed out particular references contained in the prior arts of record in the body of

claim, other passages and figures may apply as well. It is respectfully requested from the applicant, in

preparing the response, to consider fully the entire references as potentially teaching all or part of the

claimed invention, as well as the context of the passage as taught by the prior arts or disclosed by the

examiner.

Conclusion

9. Any inquiry concerning this communication or earlier communications from the examiner should

be directed to Kambiz Abdi whose telephone number is (703) 305-3364. The examiner can normally be

reached on 9:30 AM to 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor,

James P. Trammell can be reached on (703) 305-9768.

Any inquiry of a general nature or relating to the status of this application or proceeding should be

directed to the Receptionist whose telephone number is (703) 308-1113.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks Washington, D.C. 20231

or faxed to:

(703) 872-9306 [Official communications; including After Final communications labeled "Box AF"]

(703) 746-7749 [Informal/Draft communications, labeled "PROPOSED" or "DRAFT"]

Hand delivered responses should be brought to:

Crystal Park 5, 2451 Crystal Drive 7th floor receptionist, Arlington, VA, 22202

Abdi/K April 1, 2004

hw. Jayes